

What is claimed is:

1                   1. A video imaging system comprising:

2                   a substrate of a semiconductor material;

3                   an imaging array formed in said substrate and having a plurality of  
4 picture elements (pixels) each pixel being adapted to receive light and convert the  
5 light to an electrical signal, said imaging array having an output port for providing  
6 the electrical signals from the pixels representing an active video image;

7                   a first circuit formed in said substrate and connected to receive the  
8 pixel signals and for analyzing the pixel signals to provide a defective pixel output  
9 signal indicating if any one pixel of the plurality of pixels in the imaging array is  
10 defective, as the pixel signal corresponding to the one pixel is processed by the  
11 first circuit.

1                   2. A video imaging system in accordance with claim 1 wherein  
2 the first circuit includes:

3                   a filter that is responsive to a plurality of pixel values adjacent to the  
4 one pixel to provide an output signal; and

5                   circuitry including a comparator which compares the one pixel and to  
6 the output signal of the filter to determine if the one pixel is defective.

1                   3. A video imaging system in accordance with claim 2, wherein  
2 the comparator further includes hysteresis circuitry that establishes a range of  
3 values including a value of the output signal of the filter and indicates that the one  
4 pixel is defective only when the one pixel has a value that is outside of the range of  
5 values.

1                   4. A video imaging system in accordance with claim 3 further  
2 comprising a gain control circuit, formed in said substrate, the gain control circuit  
3 providing a camera pixel gain control output signal, wherein the hysteresis  
4 circuitry is responsive to the gain control output signal for establishing the range of  
5 values.

1           5.     A video imaging system in accordance with claim 2 wherein:

2                 the imager further includes optics for focusing an image onto the  
3     imaging array, the optics having a modulation transfer function (MTF); and

4                 the filter has a frequency response characteristic that approximates  
5     the MTF of the optics.

1           6.     A video imaging system in accordance with claim 2, wherein  
2     the filter is an averaging filter that averages the plurality of adjacent pixel values to  
3     produce the output signal.

1           7.     A video imaging system in accordance with claim 1 further  
2     comprising a second circuit, formed in said substrate, coupled to receive the  
3     defective pixel output signal, the one pixel and a corrected version of the one  
4     pixel, the multiplexer providing the corrected pixel when the defective pixel output  
5     signal indicates that the one pixel is defective.

1           8.     A video imaging system in accordance with claim 7, wherein  
2     the second circuit further comprises a histogramming circuit, which generates the  
3     corrected pixel from the pixel signals provided by the imaging array.

1           9.     A video imaging system in accordance with claim 7, wherein  
2     the second circuit further comprises a filter which filters the pixel signals  
3     representing respective pixel values adjacent to the one pixel to generate the  
4     corrected pixel.

1           10.    A video imaging system in accordance with claim 7, wherein:

2                 the first circuit includes:

3                     a filter that is responsive to a plurality of pixel values adjacent  
4     to the one pixel to provide an output signal; and

5                     circuitry including a comparator which compares the one pixel  
6     and to the output signal of the filter to determine if the one pixel is  
7     defective; and

8 the second circuit is coupled to the first circuit to receive the output  
9 signal of the filter as the corrected pixel.

1 11. An video imaging system in accordance with claim 7 in which  
2 the imaging array is a CMOS imager and the first and second circuits are CMOS  
3 circuits formed concurrently with the CMOS imager.

1 12. A method for processing video signals representing at least one  
2 video image, each video image including a plurality of picture element (pixel)  
3 signals, the method comprising the steps of:

4 analyzing the pixel signals to provide a defective pixel output signal  
5 indicating if any one pixel of the plurality of pixels in the at least one video image  
6 is defective, including the steps of:

7 filtering a plurality of pixel values adjacent to the one pixel in  
8 the at least one video image to provide an output signal;

9 processing the output signal to establish a range of values  
10 including the output signal; and

11 comparing the one pixel to the range of values to provide a  
12 defective pixel output signal if the one pixel has a value outside of the range  
13 of values.

1 13. A method for processing video signals in accordance with  
2 claim 12 further comprising the steps of:

3 modifying the pixel signals in magnitude responsive to a gain control  
4 signal; and

5 modifying the range of values responsive to the gain control signal.

1 14. A method for processing video signals in accordance with  
2 claim 12, wherein the video images are formed in accordance with a modulation  
3 transfer function (MTF); and

4 the step of filtering the plurality of pixel values adjacent to the one  
5 pixel value includes the step of filtering the plurality of pixel values with a  
6 frequency response characteristic that approximates the MTF to produce the output  
7 signal.

1 15. A method for processing video signals in accordance with  
2 claim 12, wherein the video images are formed in accordance with a modulation  
3 transfer function (MTF); and

4 the step of filtering the plurality of pixel values adjacent to the one  
5 pixel value includes the step of averaging the plurality of adjacent pixel values to  
6 produce the output signal.

1 16. A method for processing video signals in accordance with  
2 claim 12, further including the step of replacing the one pixel with a corrected  
3 version of the one pixel when the defective pixel output signal indicates that the  
4 one pixel is defective.

1 17. A method for processing video signals in accordance with  
2 claim 16, further includes the step of generating a histogram of the pixels in at  
3 least a portion of the video image including the one pixel and selecting a most  
4 frequently occurring pixel to generate the corrected version of the one pixel.

1 18. A method for processing video signals in accordance with  
2 claim 16, further includes the step of filtering the plurality of pixels to generate the  
3 corrected pixel.

1 19. A method for processing video signals in accordance with  
2 claim 16, further including the step of applying the output signal of the filtering  
3 step as the corrected pixel.

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